## S1C17 Manual errata

ITEM: Setting clock source					
Object manuals	Document codes	Items	Pages		
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Emulation Library Manual	413120205	in Application Program	4		

## (Error)

The EEPROM write function in this library changes the CPU clock and T16 Ch.0 configurations so as to optimize the flash memory programming timing. These configurations can be changed by rewriting the "OscClockSourceInitialize()" and "OscClockSourceFinalize()" functions defined in "OscControl.c."

The "OscClockSourceInitialize()" function configures the CPU clock and the T16 Ch.0 for writing data to the EEPROM. When rewriting this function, be sure to note the following points.

- By default, the EEPROM write function uses a 4 MHz clock that is optimum for EEPROM operations.
- When changing the clock configuration, the clock division ratio should be set so that the clock frequency does not exceed 4 MHz.
- Use the same clock source for both the CPU and T16 Ch.0.

## (Correct)

Set the system clock and T16 ch.0 with the "OscClockSourceInitialize ()" and "OscClockSourceFinalize ()" functions of "OscControl.c". The contents of each function are as follows.

Table 2.1.1 Functions of "OscControl.c"

Function name	Contents	
OscClockSourceInitialize()	Set the system clock and T16 ch.0 to the appropriate	
	speeds to use this library.	
	♦ Change the settings of the user program.	
OscClockSourceFinalize()	Set the system clock and T16 ch.0 back to the user	
	program settings.	

NOTE: This function is used in this library. Therefore, the user must check the source code of this function and modify it if necessary.

To use this library, use the above function and set the following appropriate operating

speed.

Table 2.1.2 Operating speed of system clock and T16ch.0

Model name	This library version	System clock	T16 ch.0
S1C17M20/21/22/23/	Before Ver1.3x	OSC3=16MHz	OSC3=16MHz*1
24/25		Division ratio 1/4	
	After Ver1.4x	OSC3=16MHz	OSC3=16MHz*1
		Division ratio 1/1	
S1C17M30/31/32/34	Before Ver2.2	OSC3=16MHz	OSC3=16MHz*1
		Division ratio 1/4	
	After Ver2.3	OSC3=16MHz	OSC3=16MHz*1
		Division ratio 1/1	
S1C17M33	Before Ver2.2	OSC3=16MHz	OSC3=16MHz*1
		Division ratio 1/4	
	After Ver2.3	OSC3=16MHz	OSC3=16MHz*1
		Division ratio 1/1	
S1C17W18	All	OSC3=4MHz	OSC3=4MHz*1
		Division ratio 1/1	
S1C17W36	All	OSC3=4MHz	OSC3=4MHz*1
		Division ratio 1/1	

<sup>\*1</sup> The division ratio of T16 ch.0 is set in this library.