The correspondence table of ICDmini ver. vs S1C17 models

	ICDmini	ICDmini	ICDmini	ICDmini			
	Ver.1.0 (S5U1C17001	Ver.1.1 (S5U1C17001	Ver.2.0 (S5U1C17001	Ver.3.0 (S5U1C17001	Remarks		
	H1100)	H1200)	H2100)	H3100)			
S1C17F13	*1, *2	*1, *2	V	V			
S1C17F57	*2	*2	V	V			
S1C17M01	*2	*2	V	V			
S1C17M10	*1, *2	*1, *2	V	V			
S1C17M12	*2	*2	V	V			
S1C17M13	*2	*2	V	V			
S1C17M20	*1, *2	*1, *2	V	V			
S1C17M21	*1, *2	*1, *2	V	V			
S1C17M22	*1, *2	*1, *2	V	V			
S1C17M23	*1, *2	*1, *2	V	V			
S1C17M24	*1, *2	*1, *2	V	V			
S1C17M25	*1, *2	*1, *2	V	V			
S1C17M30	*1, *2	*1, *2	V	V			
S1C17M31	*1, *2	*1, *2	V	V			
S1C17M32	*1, *2	*1, *2	V	V			
S1C17M33	*1, *2	*1, *2	V	V			
S1C17M34	*1, *2	*1, *2	V	V			
S1C17V**	Please cont	act our sales rep	resentative.				
S1C17W03	*2	*2	V	V			
S1C17W04	*2	*2	V	V			
S1C17W12	*2	*2	V	V			
S1C17W13	*2	*2	V	V			
S1C17W14	*2	*2	V	V			
S1C17W15	*2	*2	V	V			
S1C17W16	*2	*2	✓	V			
S1C17W18	*1, *2	*1, *2	✓	V			
S1C17W22	*2	*2	✓	V			
S1C17W23	*2	*2	✓	V			
S1C17W34	*1, *2	*1, *2	✓	V			
S1C17W35	*1, *2	*1, *2	✓	V			
S1C17W36	*1, *2	*1, *2	V	V			
S1C17Y**	Please contact our sales representative.						
S1C17001	-	-	-	-	Mask ROM		
S1C17002	-	-	-	-	Mask ROM		
S1C17003	-	_	_	-	Mask ROM		
S1C17121	-	-	-	-	Mask ROM		
S1C17153	-	-	-	-	Mask ROM		
S1C17501	V	V	V	V			
S1C17554	*2	*2	V	V			
S1C17555	*1, *2	*1, *2	V	V			
S1C17564	*2	*2	V	V			
S1C17565	*1, *2	*1, *2	V	V			
S1C17589	*2	*2	V	V			
S1C17601	V	V	V	V			
S1C17602	V	V	V	~			
S1C17604	V	V	V	V			
S1C17611	V	V	V	V			
S1C17621	V	V	V	V			
S1C17622	V	V	V	V			
S1C17624	V	V	V	V			
S1C17651	*2	*2	V	V			
S1C17653	*2	*2	V	V			
S1C17656	*2	*2	V	V			

The correspondence table of ICDmini ver. vs S1C17 models

	ICDmini Ver.1.0 (S5U1C17001 H1100)	ICDmini Ver.1.1 (S5U1C17001 H1200)	ICDmini Ver.2.0 (S5U1C17001 H2100)	ICDmini Ver.3.0 (S5U1C17001 H3100)	Remarks
S1C17701	V	V	V	V	
S1C17702	V	V	V	V	
S1C17703	V	V	V	V	
S1C17704	V	V	V	V	
S1C17705	V	V	V	V	
S1C17706	V	V	V	V	
S1C17711	V	V	V	V	
S1C17801	V	V	V	V	
S1C17803	V	V	V	*3	
S1C17955	*1, *2	*1, *2	V	V	
S1C17965	*1, *2	*1, *2	V	V	
S7C17M11	*1, *2	*1, *2	V	V	
Others	Please cont				

Remarks

- Available.
- *1 It is available when VPP power is generated in MCU internally.*
- *2 It is available when supplying VPP power from the outside using a stabilized power supply.*
- *3 It is available when the CPU operation clock is 24MHz or less.

 *For details, please refer to readme file in the MCU model information file prepared for each model.

Notes

Please confirm the firmware of each ICDmini by checking our website and writing the latest version. Seiko Epson reserves the right to make changes to this material without notice. Seiko Epson Corporation ${\sf Jan/2018}$