

CRYSTAL OSCILLATOR Low Profile / High stability SPXO

SG-150S*E

: 3.000 MHz to 54.000 MHz •Frequency range Supply voltage : 1.8 V / 2.5 V / 3.3 V

•Current consumption : 1.2 mA Typ.

(SEE: 1.8 V No load condition 40 MHz)

Standby(ST) •External dimensions : 2.1 × 1.7 × 0.75 mm



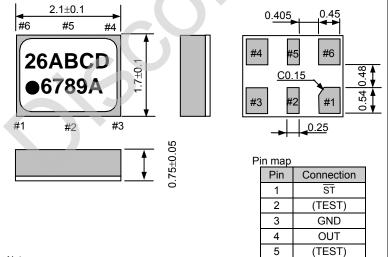
Specifications (characteristics)

Item	Symbol	Specifications			Conditions / Domestic
		SG-150SEE	SG-150SDE	SG-150SCE	Conditions / Remarks
Output frequency range	fo	3.000 MHz to 54.000 MHz			Please contact us for inquiries regarding the available frequencies.
Supply voltage	Vcc	1.8 V Typ. 1.6 V to 2.2 V	2.5 V Typ. 2.2 V to 2.7 V	3.3 V Typ. 2.7 V to 3.6 V	
Storage temperature	T_stg	-40 °C to +85 °C			Store as bare product.
Operating temperature	T_use	-40 °C to +85 °C			
Frequency tolerance *	f_tol -	D: ±20 × 10 ⁻⁶ , E: ±15 × 10 ⁻⁶			-20 °C to +70 °C Vcc +10%
		H: ±20 × 10 ⁻⁶ , T: ±15 × 10 ⁻⁶			-40 °C to +85 °C
Current consumption	Icc	2.3 mA Max.	2.5 mA Max.	3.5 mA Max.	No load condition,3 MHz≤fo≤32 MHz
		2.8 mA Max.	3.0 mA Max.	4.0 mA Max.	No load condition,32 MHz <fo≤40 mhz<="" td=""></fo≤40>
		3.3 mA Max.	3.5 mA Max.	4.5 mA Max.	No load condition,40 MHz <fo≤48 mhz<="" td=""></fo≤48>
		4.5 mA Max.	5.0 mA Max.	6.0 mA Max.	No load condition,48 MHz <fo≤54 mhz<="" td=""></fo≤54>
Stand-by current	I_std	5.0 μA Max.			ST =GND
Symmetry	SYM	45 % to 55 %			50 % Vcc level,L_CMOS ≤ 15 pF
Output voltage	Voн	90 % Vcc Min.		IOH=-4 mA	
	Vol	10 % Vcc Max.			IoL= 4 mA
Output load condition (CMOS)	L_CMOS	15 pF Max.			
Input voltage	VIH	80 % Voc Min. 20 % Voc Max.			ST terminal
	VIL				
Rise time / Fall time	tr/ tf	4.5 ns Max.			20 % Vcc to 80 % Vcc level, L_CMOS=15 pF
Start-up time	t_str	5 ms Max.			t=0 at 90 % Vcc
Frequency aging	f_aging	This is included in frequency tolerance specification.			+25 °C, First year, Vcc= 1.8 V, 2.5 V, 3.3 V

^{*} Please contact us for inquiries regarding available frequency tolerance.

External dimensions





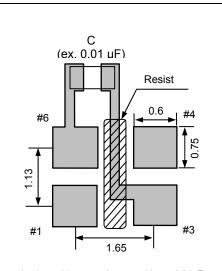
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Vcc

 $\overline{\text{ST}}$ pin = HIGH : Specified frequency output. ST pin = LOW : Output is high impedance, oscillation stops.

TEST pin: Do not connect externally. (Recommend the design without the footprint.)

Footprint (Recommended)



To maintain stable operation, provide a 0.01uF to 0.1uF by-pass capacitor at a location as near as possible to the power source terminal of the crystal product (between Vcc - GND).

PROMOTION OF ENVIRONMENTAL MANAGEMENT SYSTEM CONFORMING TO INTERNATIONAL STANDARDS

At Seiko Epson, all environmental initiatives operate under the Plan-Do-Check-Action (PDCA) cycle designed to achieve continuous improvements. The environmental management system (EMS) operates under the ISO 14001 environmental management standard.

All of our major manufacturing and non-manufacturing sites, in Japan and overseas, completed the acquisition of ISO 14001 certification.

ISO 14000 is an international standard for environmental management that was established by the International Standards Organization in 1996 against the background of growing concern regarding global warming, destruction of the ozone layer, and global deforestation.

WORKING FOR HIGH QUALITY

In order provide high quality and reliable products and services than meet customer needs,

Seiko Epson made early efforts towards obtaining ISO9000 series certification and has acquired ISO9001 for all business establishments in Japan and abroad. We have also acquired ISO/TS 16949 certification that is requested strongly by major automotive manufacturers as standard.

ISO/TS16949 is the international standard that added the sector-specific supplemental requirements for automotive industry based on ISO9001.

Explanation of the mark that are using it for the catalog



►Pb free.



- ► Complies with EU RoHS directive.
 - *About the products without the Pb-free mark.

 Contains Pb in products exempted by EU RoHS directive.

 (Contains Pb in sealing glass, high melting temperature type solder or other.)



▶ The products have been designed for high reliability applications such as Automotive.

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