

CRYSTAL OSCILLATOR (SPXO) OUTPUT : HCSL



Product Number SG2016HHN: X1G006231xxxx15 SG2520HHN: X1G005931xxxx15

SG2016HHN / SG2520HHN

 Frequency range
 Supply voltage
 Frequency tolerance
 Operating temperature
 Function

25 MHz to 500 MHz 2.5 V Typ. / 3.3 V Typ. ±20 × 10⁻⁶

: ±20 × 10⁻⁶ re : -40 °C to +85 °C, -40 °C to +105 °C

SG2016HHN V) (2.0 × 1.6 × 0.63 mm)



SG2520HHN (2.5 × 2.0 × 0.74 mm)

 •Function
 : Output enable (OE) or Standby (ST)

 •Phase jitter
 : 90 fs Max. (100 MHz < fo ≤ 156 MHz)</td>

•Phase jitter : 90 fs Max. (100 MHz < fo \leq 156 MHz, V_{CC} = 2.5 V, 3.3 V) •PCIe Gen5,6 Jitter specification compliant

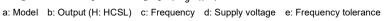
Specifications (characteristics)

Item	Symbol	Specifications	Conditions / Remarks
Output frequency range	fo	25 MHz to 500 MHz	Please contact us for available frequencies.
Supply voltage	Vcc	D: 2.5 V ± 5 %, C: 3.3 V ± 5 %	
Storage temperature range	T stg	-55 °C to +125 °C	
Operating temperature range	T use	G: -40 °C to +85 °C, H: -40 °C to +105 °C	
Frequency tolerance	_	,	Includes initial frequency tolerance, frequency /
	f_tol	C: ±20 × 10 ⁻⁶ Max.	temperature characteristics, frequency / voltage coefficient and 10 years aging (+25 °C)
Current consumption		35 mA Max.	$25 \text{ MHz} \le \text{fo} < 212 \text{ MHz}$ OE or $\overline{ST} = V_{CC}$.
	lcc	40 mA Max.	$212 \text{ MHz} \le \text{fo} < 500 \text{ MHz}$ L HCSL = 50 Ω
Disable current	I dis	25 mA Max.	OE = GND
Stand-by current	_	30 µA Max.	ST = GND, T use Max. = +85 °C
	I_std	60 µA Max.	\overline{ST} = GND. T use Max. = +105 °C
Symmetry	SYM	45 % to 55 %	At output crossing point
ey	0.111	0.5 V to 0.7 V	$25 \text{ MHz} \le \text{fo} \le 212 \text{ MHz}$
		0.4 V to 0.65 V	$\frac{212 \text{ MHz} = 10 \text{ (212 MHz})}{212 \text{ MHz}}$ Output option: A
Output voltage	Vон	0.6 V to 0.8 V	$25 \text{ MHz} \le \text{fo} \le 212 \text{ MHz}$
output voltago		0.5 V to 0.75 V	$\frac{212 \text{ MHz} = 10 \text{ (212 MHz})}{212 \text{ MHz}}$ Output option: B
	Voi	-0.15 V to +0.15 V	
		0.7 V to 1.4 V	Output option: A
Differential swing	Vsw	0.8 V to 1.6 V	Output option: B
Crossing voltage	V _{CR}	0.25 V to 0.55 V	
Rise time / Fall time	tr/tf	0.7 ns Max.	20 % - 80 % (Vон - Voi)
Differential output			()
rise slew rate / fall slew rate	Rr/Rf	2 V/ns to 10 V/ns	Between -0.15 V and 0.15 V of differential output
Output load condition	L_HCSL	50 Ω	
Input voltage	- VIH	70 % Vcc Min.	
	VIL	30 % V _{CC} Max.	OE or ST terminal
Output enable time	tsta oe	500 ns Max.	t = 0 at OE = 70 % V _{CC}
	tsta st	10 ms Max.	t = 0 at ST = 70 % V _{CC}
Output disable time	tstp oe	100 ns Max.	t = 0 at OE = 30 % V _{CC}
	tstp st	100 ns Max.	t = 0 at ST = 30 % V _{CC}
Start-up time	t str	10 ms Max.	t = 0 at 90 % V _{CC}
Phase jitter	_	200 fs Max.	25 MHz ≤ fo < 100 MHz Offset frequency
		90 fs Max.	100 MHz \leq fo \leq 156 MHz fo $<$ 50 MHz:
	t _{PJ}	70 fs Max.	156 MHz < fo ≤ 212 MHz 12 kHz to 5 M
		60 fs Max.	212 MHz < fo ≤ 391 MHz fo ≥ 50 MHz:
		50 fs Max.	391 MHz < fo ≤ 500 MHz 12 kHz to 20 M
Jitter	t _{c-c}	60 ps Max.	Cycle to cycle jitter (Peak to Peak)
PCIe iitter limits		0.1 ps Max.	For PCIe Gen5
for CC architecture	-	0.06 ps Max.	For PCIe Gen6

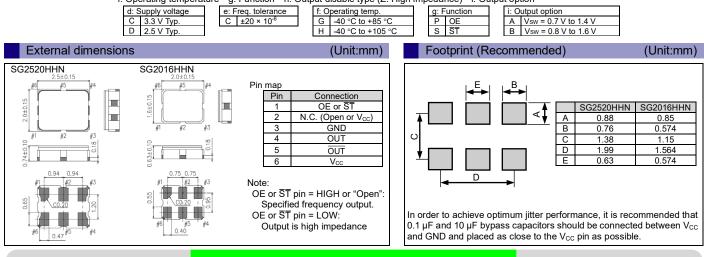
Product name

Product Name (Standard form)

SG2016 HHN 100.000000MHz C C H P Z A a b c d e f g h i



f: Operating temperature g: Function h: Output disable type (Z: High impedance) i: Output option



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At Seiko Epson, all environmental initiatives operate under the Plan-Do-Check-Action (PDCA) cycle designed to achieve continuous improvements. The environmental management system (EMS) operates under the ISO 14001 environmental management standard.

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